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				Application Number	10/039,738
				Filing Date	January 4, 2002
				First Named Inventor	Paul Magliocco
				Group Art Unit	2857
				Examiner Name	Unknown
Sheet	1	of	1	Attorney Docket Number	A-70697/ENB/WEN
<b>OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
KML		VIRGIL P. LABUDA ET AL., "DFT Standards Allow Optimized Tester Configuration to Reduce Cost of Test", The Second Annual IEEE ASIC Seminar and Exhibit. September 25-28, 1989, Rochester Riverside Convention Center, Rochester, New York. IEEE Catalog # 89TH0280-8.			
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